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T2147-907715-US3933

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Sylvie LESMANNE ET AL.

Serial No.: To be assigned.

Filed: February 15, 2002

For: **COHERENCE CONTROLLER FOR A
MULTIPROCESSOR SYSTEM,
MODULE, AND MULTIPROCESSOR
SYSTEM WITH A MULTIMODULE
ARCHITECTURE INCORPORATING
SUCH A CONTROLLER**

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: Examiner:
:
:
: Group Art Unit:
:
: Corresponding to:
: FR 01/02089
: Filed February 15, 2001
:



McLean, Virginia
February 15, 2002

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of Patents
and Trademarks
Washington, D.C. 20231

Sir:

In compliance with the duty of disclosure set forth in 37 CFR 1.56, it is requested that the documents listed on the attached Information Disclosure Statement By Applicant be made of record in this application. These documents were cited in the corresponding French application No. FR01/02089 filed on February 15, 2001. A copy of the "Rapport De Recherche Preliminaire" (Preliminary Search Report) is enclosed. In compliance with the requirements of 37 CFR 1.97 and 1.98, the possible relevance of the documents is noted on the attached Information Disclosure Statement by Applicant. Copies of the documents are enclosed herewith.

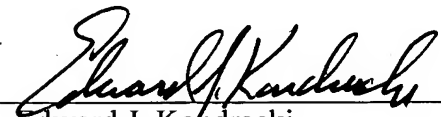
Without any assertion of prior art effect, the documents on the Information Disclosure Statement by Applicant are called to the Examiner's attention. Although the relevance of each document has been briefly indicated, the Examiner is urged to make his

own determination of relevance and no representation is made nor intended that "better"
art is not available.

Respectfully submitted,
Miles & Stockbridge P.C.

Date February 15, 2002

By:


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